## **IN THE SPECIFICATION:**

Please replace the paragraph of specification at page 1, lines 3-6 with the following replacement paragraph:

The present application is a continuation of commonly assigned copending U.S. Patent Application Serial No. 09/038,504, now issued as U.S. Patent No. 6,208,649, which was filed on March 11, 1998, by <u>Iraymond Raymond Kloth</u> for a Derived VLAN Mapping Technique and is hereby incorporated by reference.

Please replace the paragraph of specification at page 12, line 23 to page 13, line 3 with the following replacement paragraph:

There are generally two values assigned to each port of the switch: a virtual local area network (VLAN) value and an index value. The index is essentially a 10-bit, hard-coded value that uniquely identifies the port to the switch. An example of a switch configured with index values for identifying ports and suitable for use with the present invention is disclosed in copending and commonly-assigned U.S. Patent Application Serial No. 08/621,720, now issued as U.S. Patent No. 5,796,732, filed March 28, 1996, titled *Architecture for an Expandable Transaction-Based Switching Bus*, which application is hereby incorporated by reference in its entirety.

Please replace the paragraph of specification at page 16, line 26 to page 17, line 9 with the following replacement paragraph:

In one implementation, the original VLAN designation assigned to the frame at the input port of the switch is appended onto an interlink switch (ISL) header of a frame. The ISL header contains a VLAN field for carrying a VLAN value over a trunking link (such as an IEEE Std 802.1q trunking link or an ISL link) to a destination trunking port. An example of an ISL header used for transmitting information between trunking ports is disclosed in copending and commonly-assigned U.S. Patent Application Serial No. 08/623,142, now issued as U.S. Patent No. 5,742,604, filed March 28, 1996 titled *Interswitch Link Mechanism for Connecting High-Performance Network Switches*, which application is hereby incorporated by reference in its entirety. The destination switch of the destination trunking port re-derives the VLAN using the original, non-derived VLAN in accordance with the port VLAN-based mapping technique described herein.